OIPE JAN 05 2004 PATENT & TRADEMARK OFFICE

Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO.	SERIAL NO.
LIST OF REFERENCES CITED BY APPLICANT		243883US2S	10/682,110
		APPLICANT: Moto YABUKI	
		FILING DATE: October 10, 2003	GROUP 2818

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TH	AA	6,426,249	07/30/2002	R. M. GEFFKEN, et al.	438	239	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	TRANSLATION NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

TH	AW	T. OZAKI, et al., Symposium on VLSI Technology Digest of Technical Papers, pages 113-114, "A FULLY PLANALIZED 8M BIT FERROELECTRIC RAM WITH 'CHAIN' CELL STRUCTURE", 2001
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner TRAN HO — Date Considered oct 2004

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.